



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Andy L. Lee, Brian Johnson,
and Richard G. Cliff

Application No.: 09/761,609 ✓ Confirmation No.: 5825

Filed : January 16, 2001

For : SHIFT REGISTER IMPLEMENTATIONS OF
FIRST-IN/FIRST-OUT MEMORIES

Group Art Unit : 2818

Hon. Commissioner
for Patents
Washington, D.C. 20231

174/188

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INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97,
applicants wish to call the attention of the Examiner to the
following references:

U.S. patents	5,212,652 Agrawal et al.
	5,343,406 Freeman et al.
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	5,926,036 Cliff et al.
	6,049,223 Lytle et al.

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U.S. Patent Appln. No. 09/266,235 Jefferson et al.
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"XC4000XLA/XV Field Programmable Gate Arrays;
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6-170.

"Triscend E5 Configurable System-on-Chip Family",
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Product Description, cover page and pp. 25-28.

"Apex 20K Programmable Logic Device Family", Data
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March 9, 2000, Xilinx, Inc., San Jose, CA, pp.
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"VirtexTM-E 1.8V Extended Memory Field
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San Jose, CA, pp. 1 and 6.

These references are also listed on the attached
Form PTO-1449, and copies are enclosed.

Consideration of the foregoing in relation to this
patent application is respectfully requested.

Respectfully submitted,

Robert R. Jackson

Robert R. Jackson

Reg. No. 26,183

Attorney for Applicants

FISH & NEAVE

1251 Avenue of the Americas

New York, New York 10020

Tel.: (212) 596-9000

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Correspondence is being
Deposited with the U.S.
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April 9, 2001

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